

Figure 1

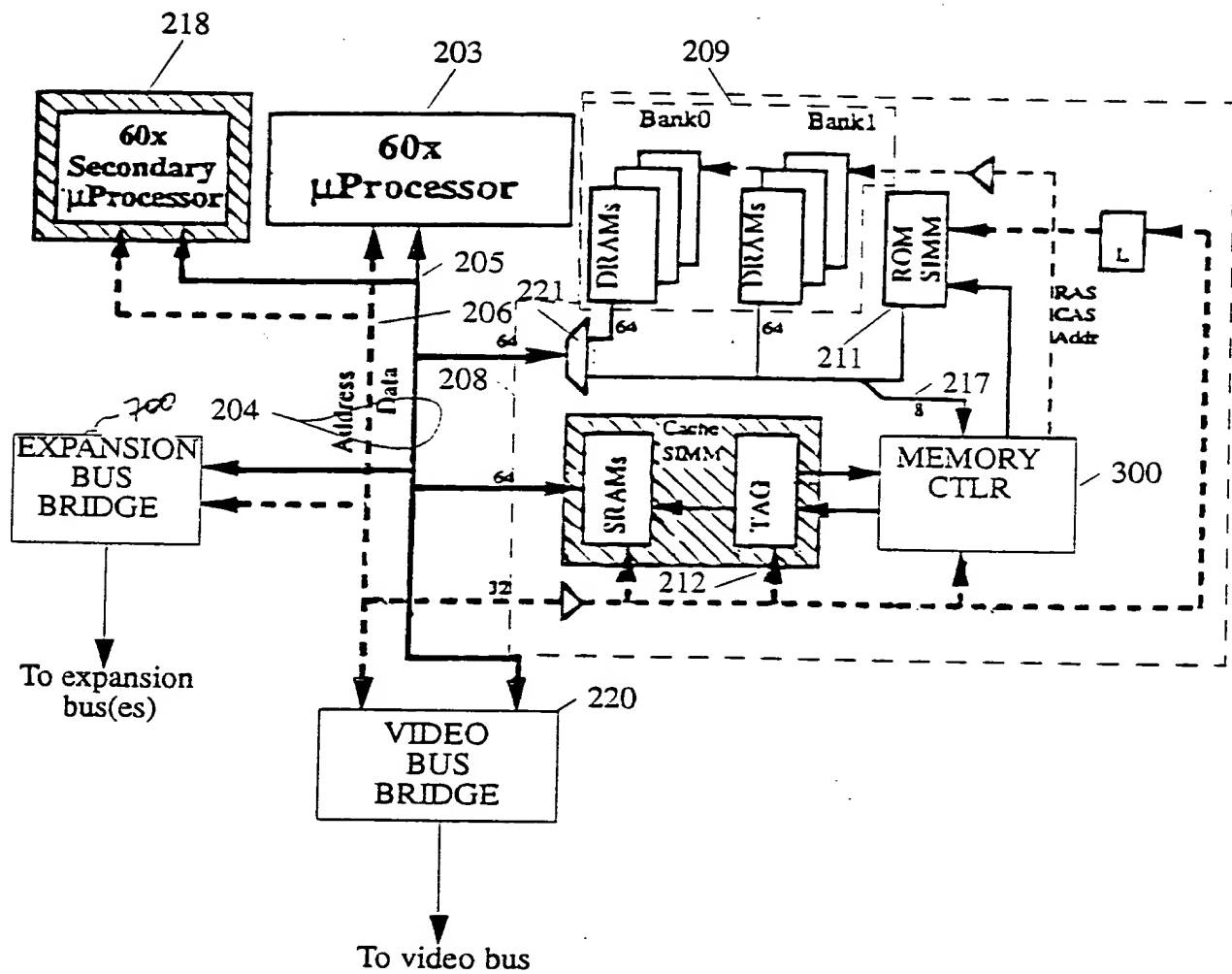


Figure 2

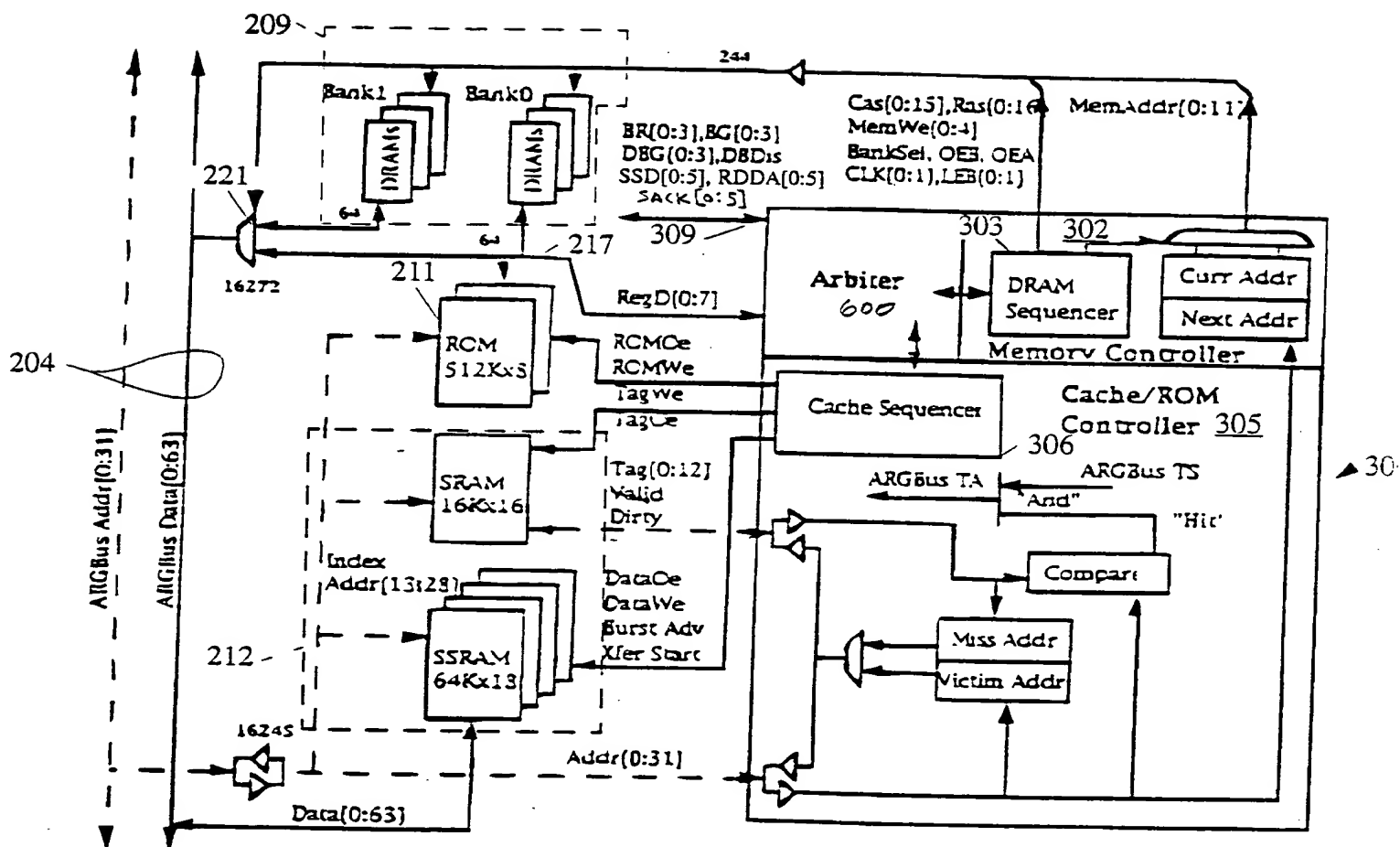
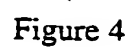


Figure 3



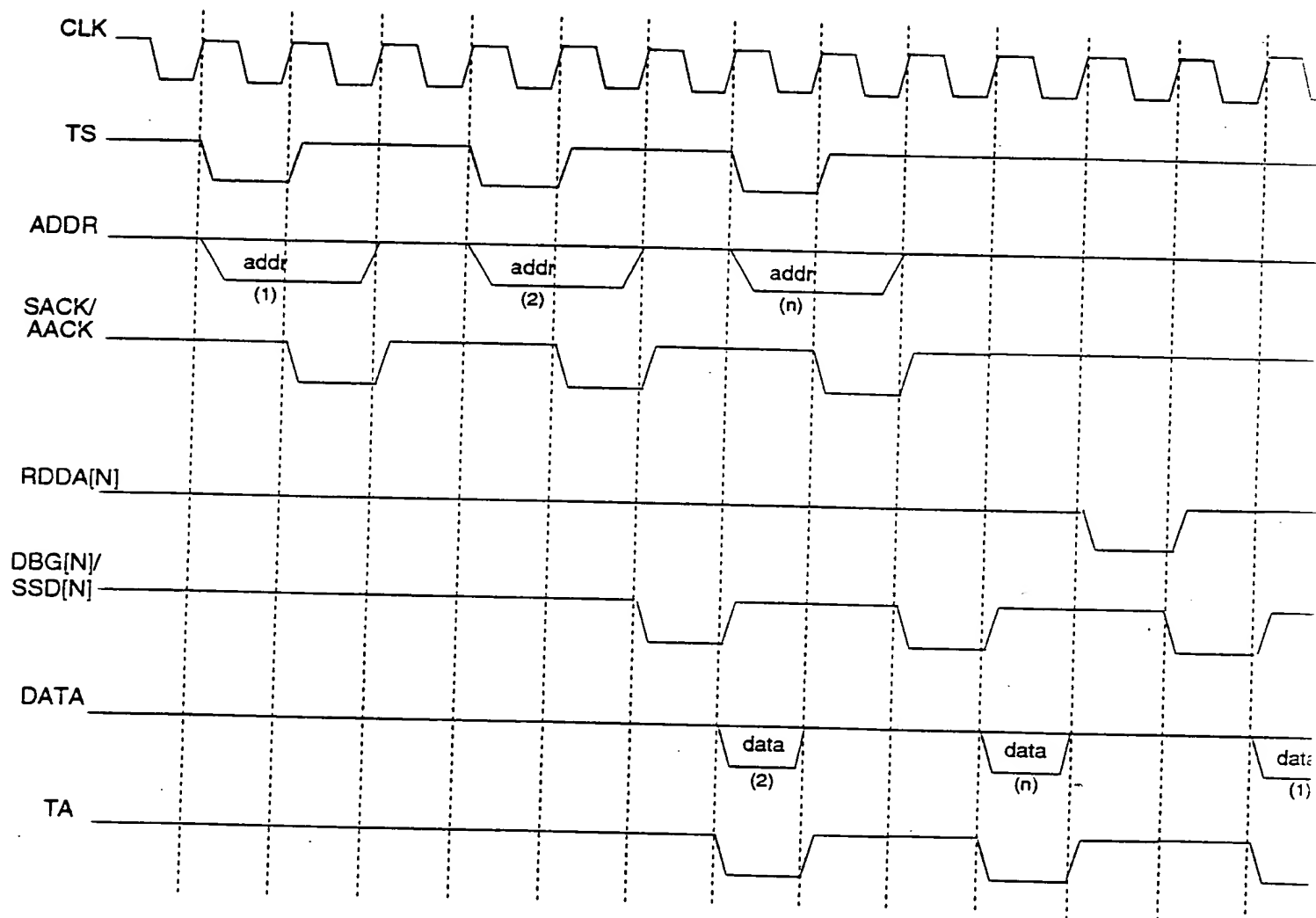


Figure 5

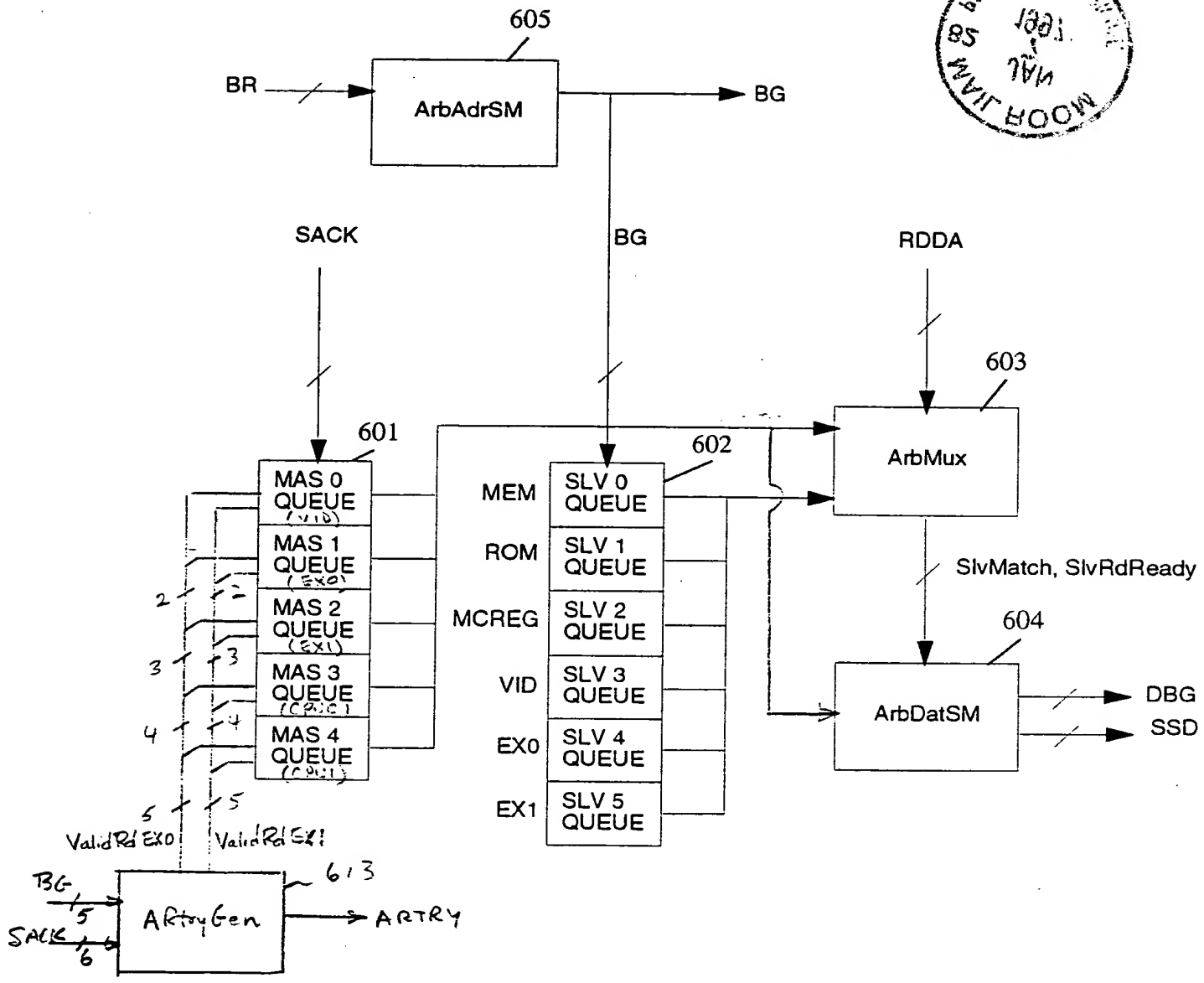
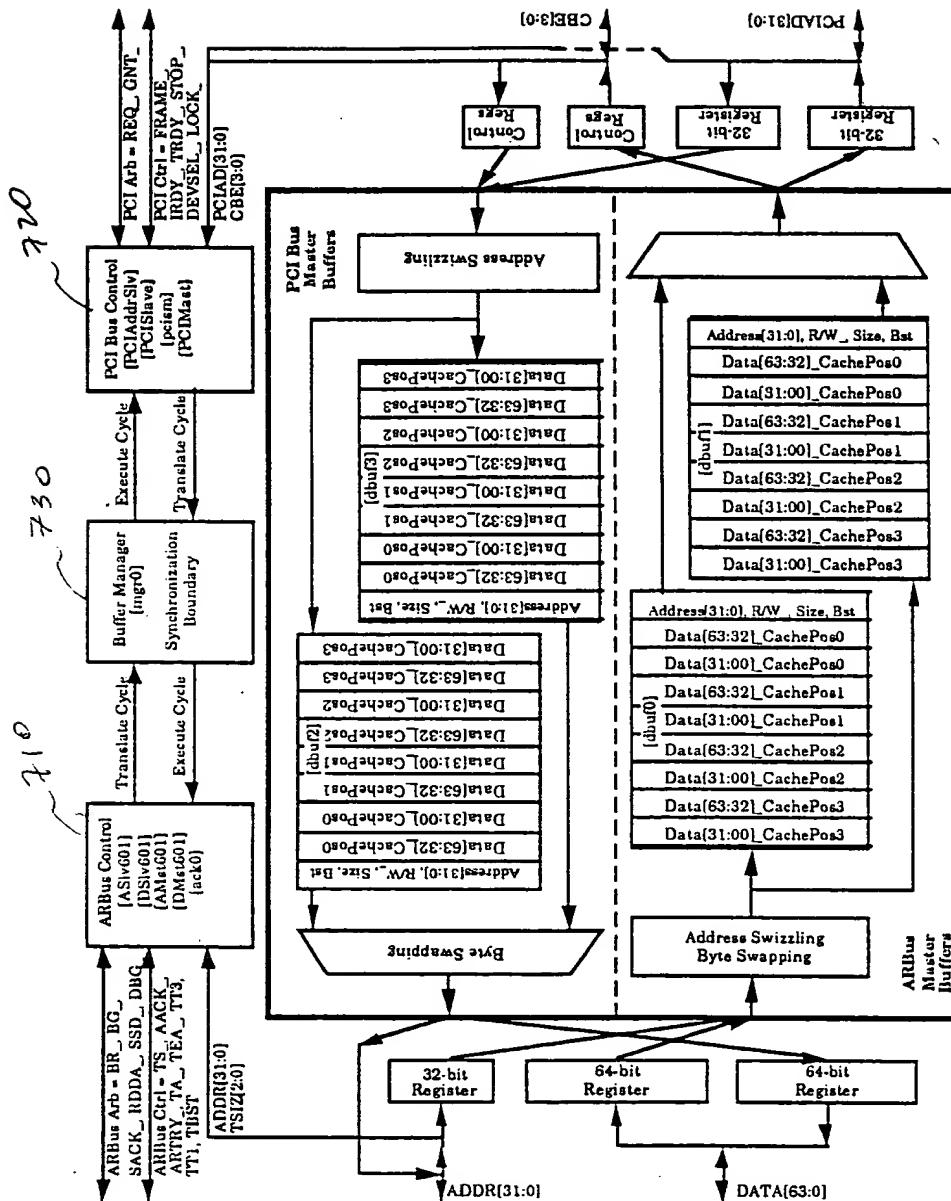


Figure 6



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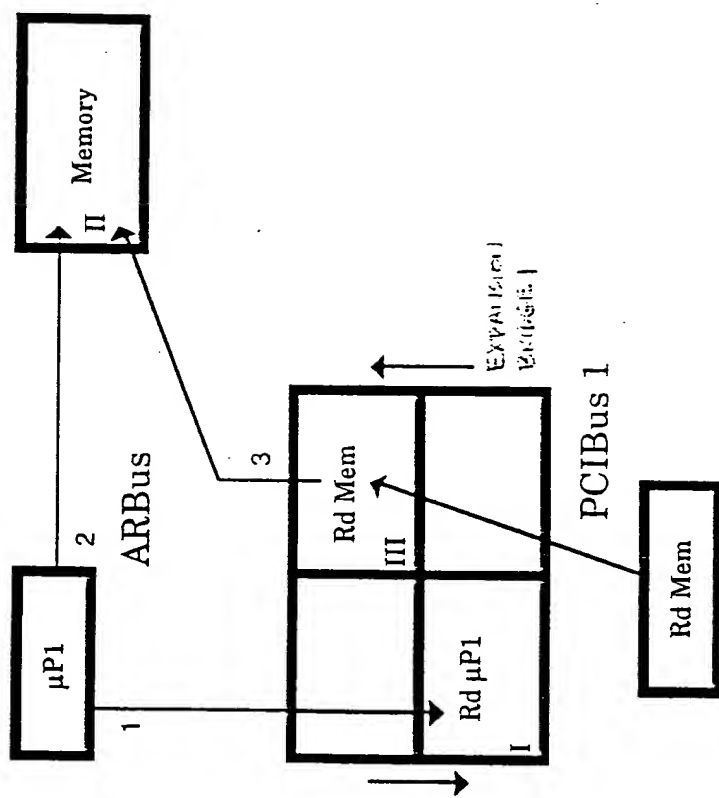


Fig 2



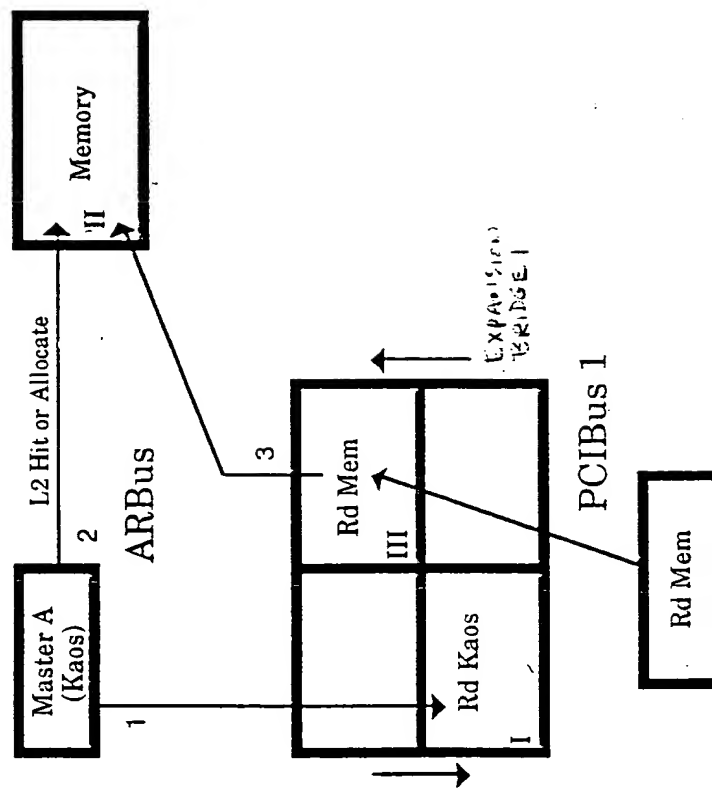
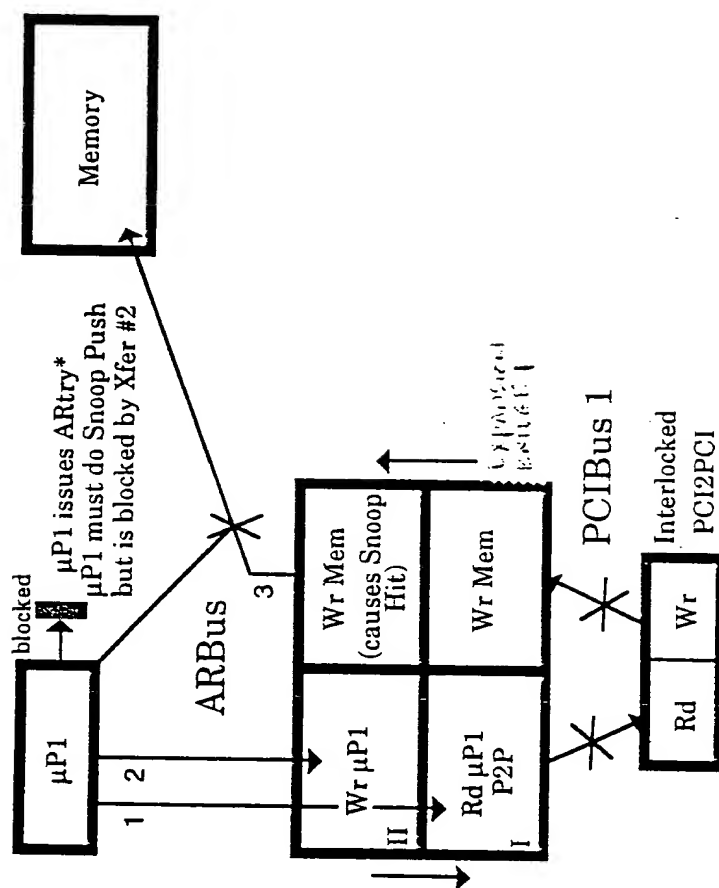


Fig 9



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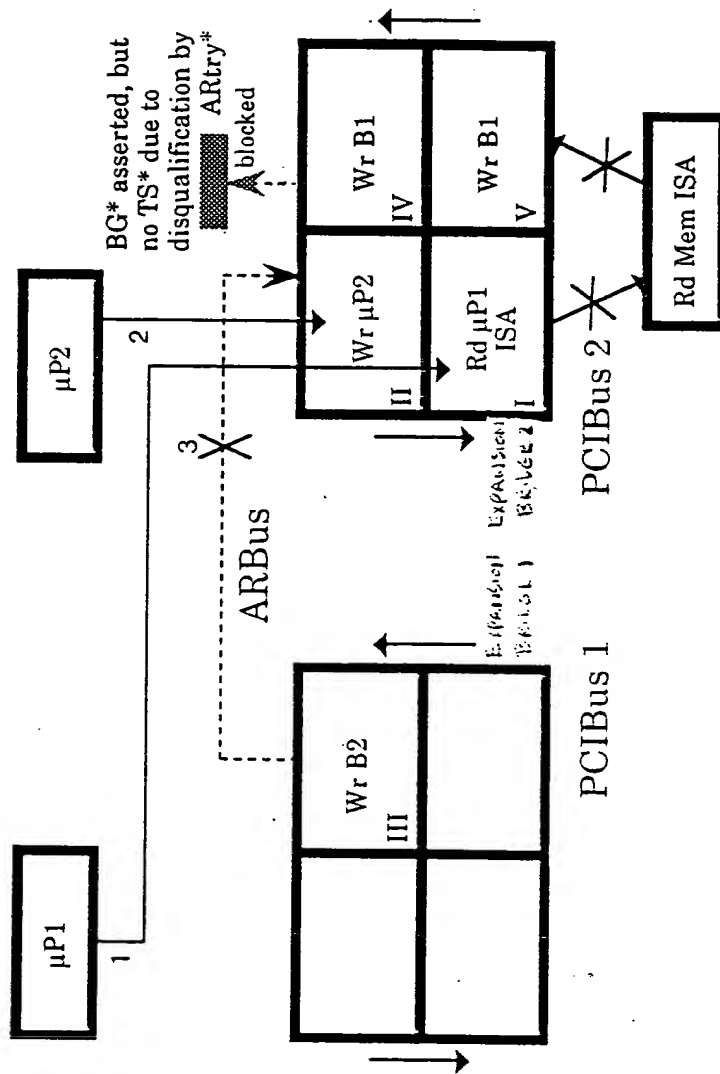
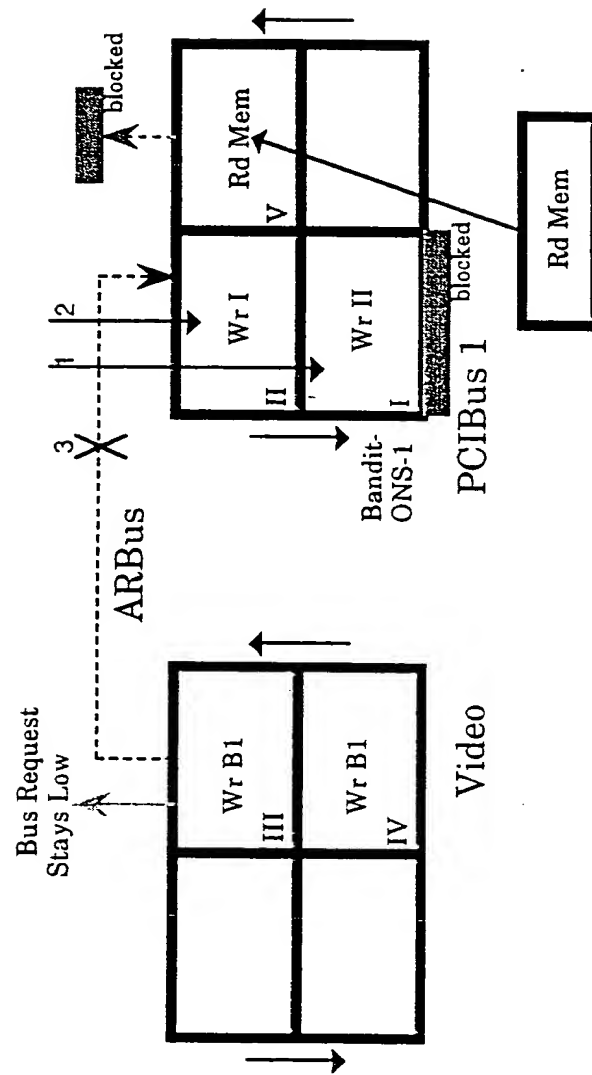
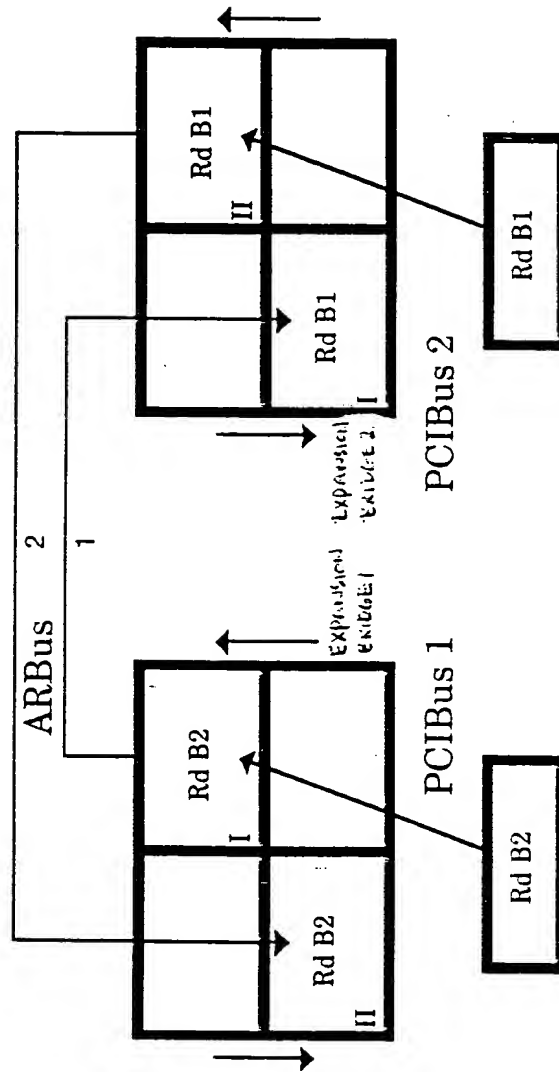


Fig 11



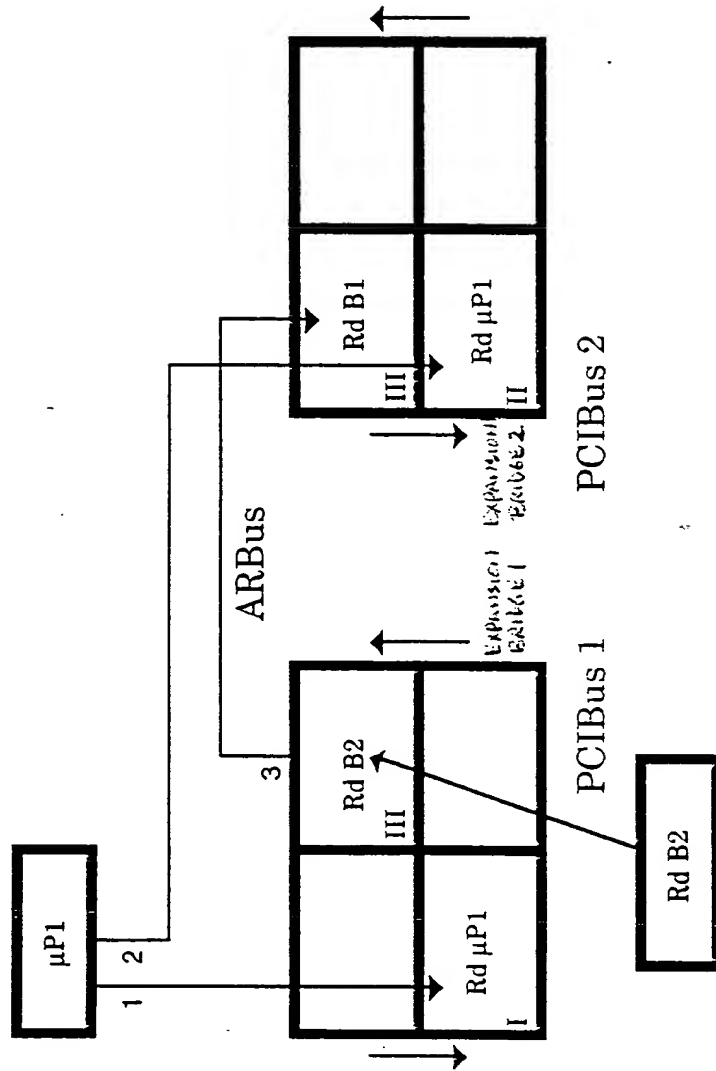


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7-16 13





7-10-14

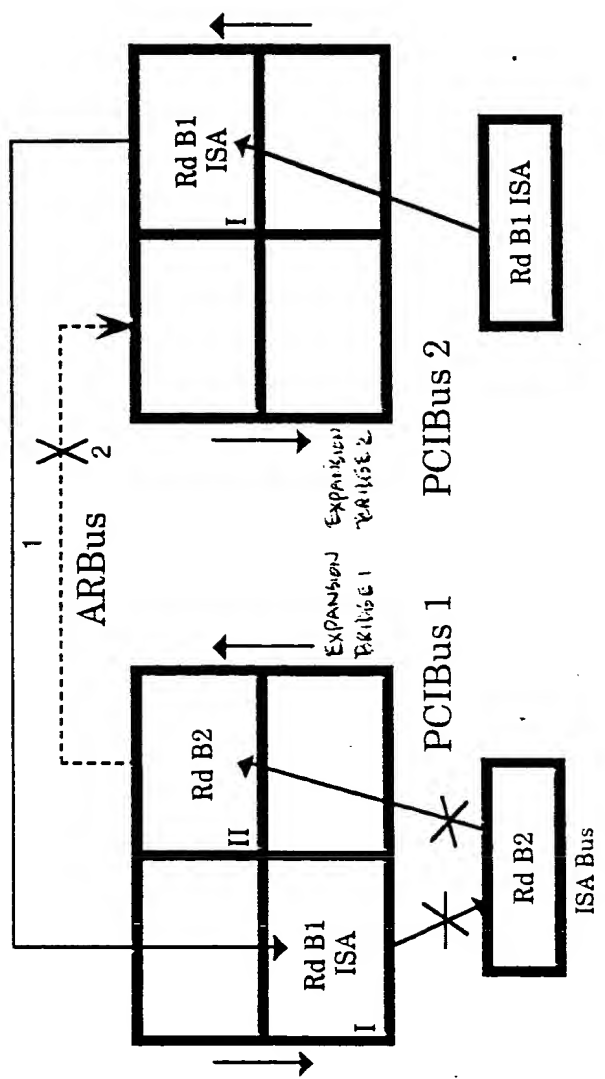


Fig 15

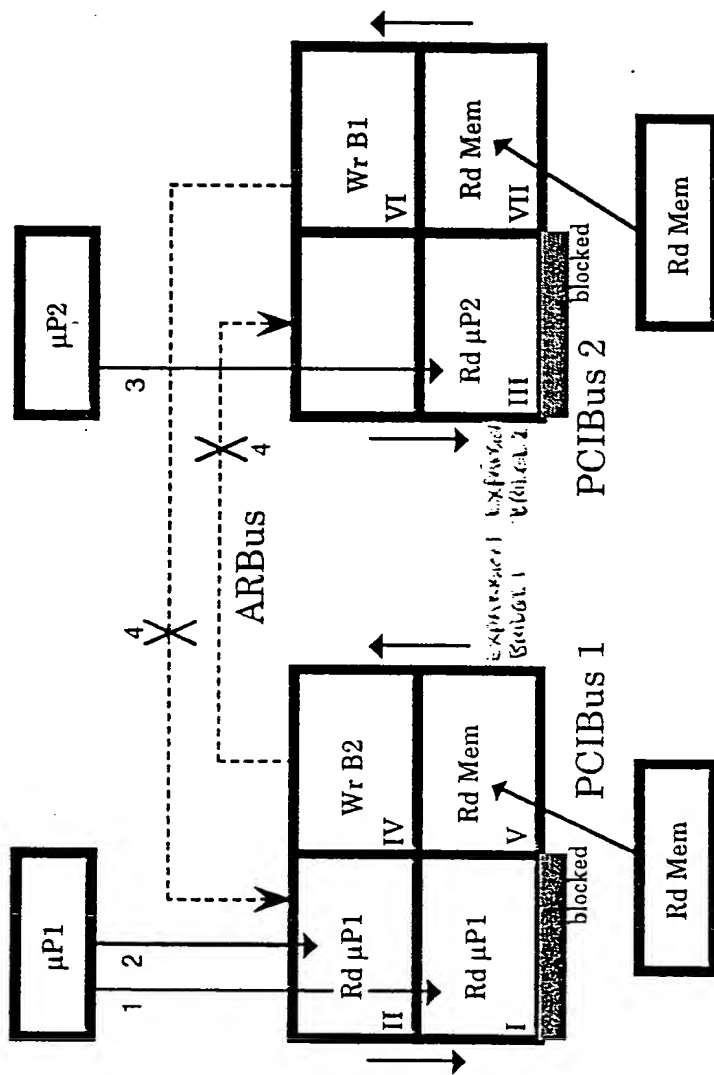


FIG 16







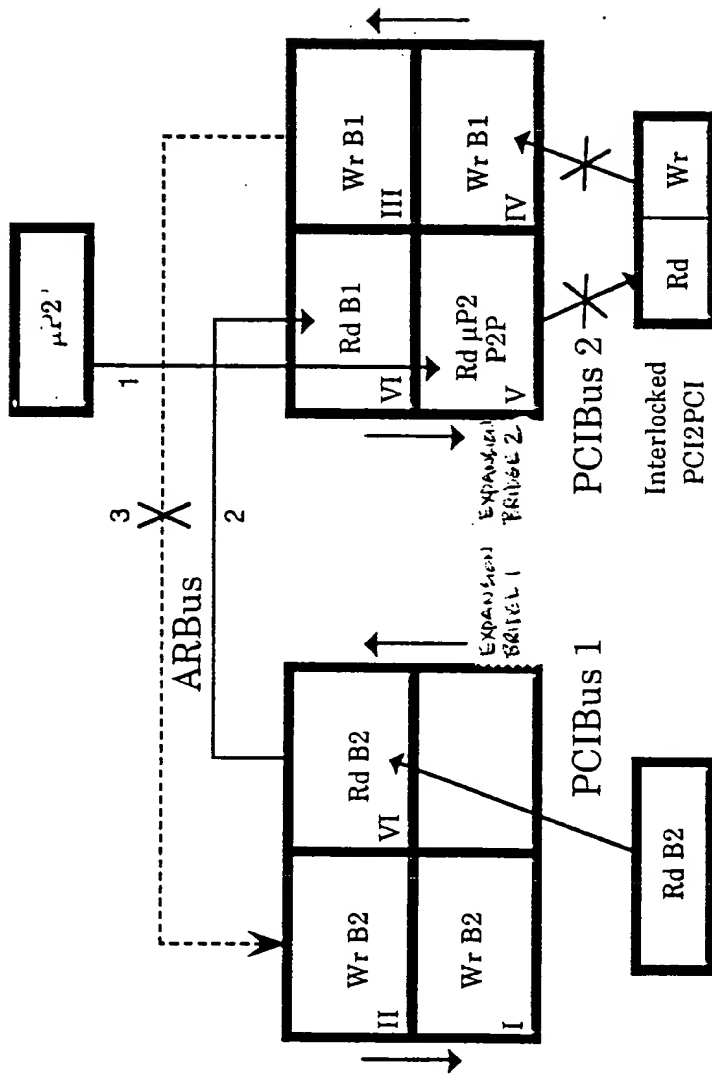
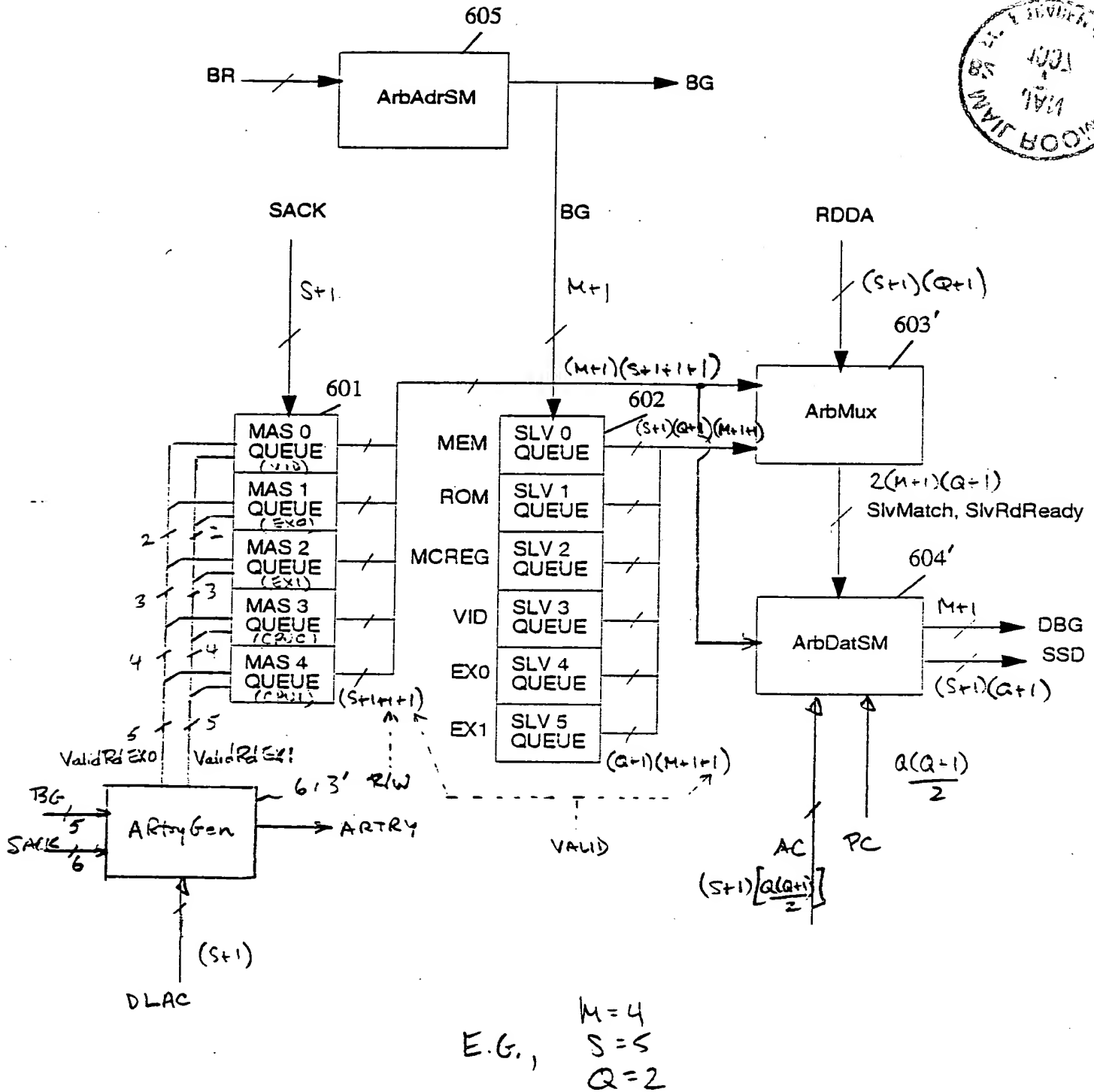
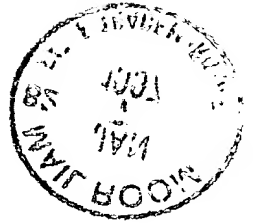


Fig. 18





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Figure 6

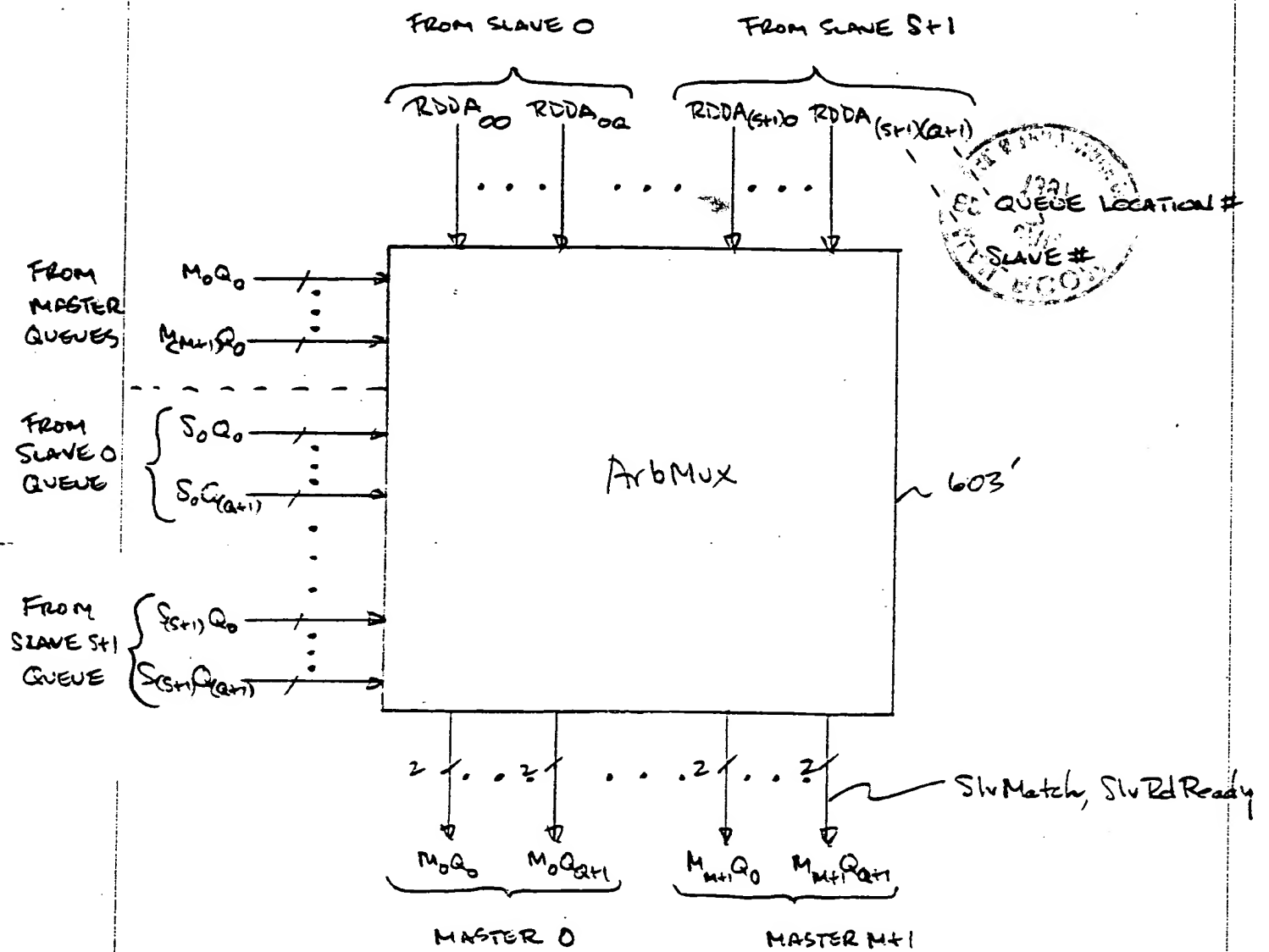


FIG. 20

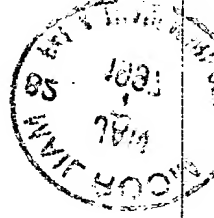
R0DA 00 11 02  
 R1DA 10 11 12  
 R2DA 20 21 22  
 R3DA 30 31 32  
 R4DA 40 41 42  
 R5DA 50 51 52

M0Q0 8  
 M1Q0 8  
 M2Q0 8  
 M3Q0 8  
 M4Q0 8  
 S0Q0 6  
 S0Q1 6  
 S0Q2 6  
 S1Q0 6  
 S1Q1 6  
 S1Q2 6  
 S2Q0 6  
 S2Q1 6  
 S2Q2 6  
 S3Q0 6  
 S3Q1 6  
 S3Q2 6  
 S4Q0 6  
 S4Q1 6  
 S4Q2 6  
 S5Q0 6  
 S5Q1 6  
 S5Q2 6

Fig 21

~603'

ArbMux



SlvMatch, SlvRdReady

M0Q0 M0Q1 M0Q2 M1Q0 M1Q1 M1Q2 M2Q0 M2Q1 M2Q2 M3Q0 M3Q1 M3Q2 M4Q0 M4Q1 M4Q2 M5Q0 M5Q1 M5Q2

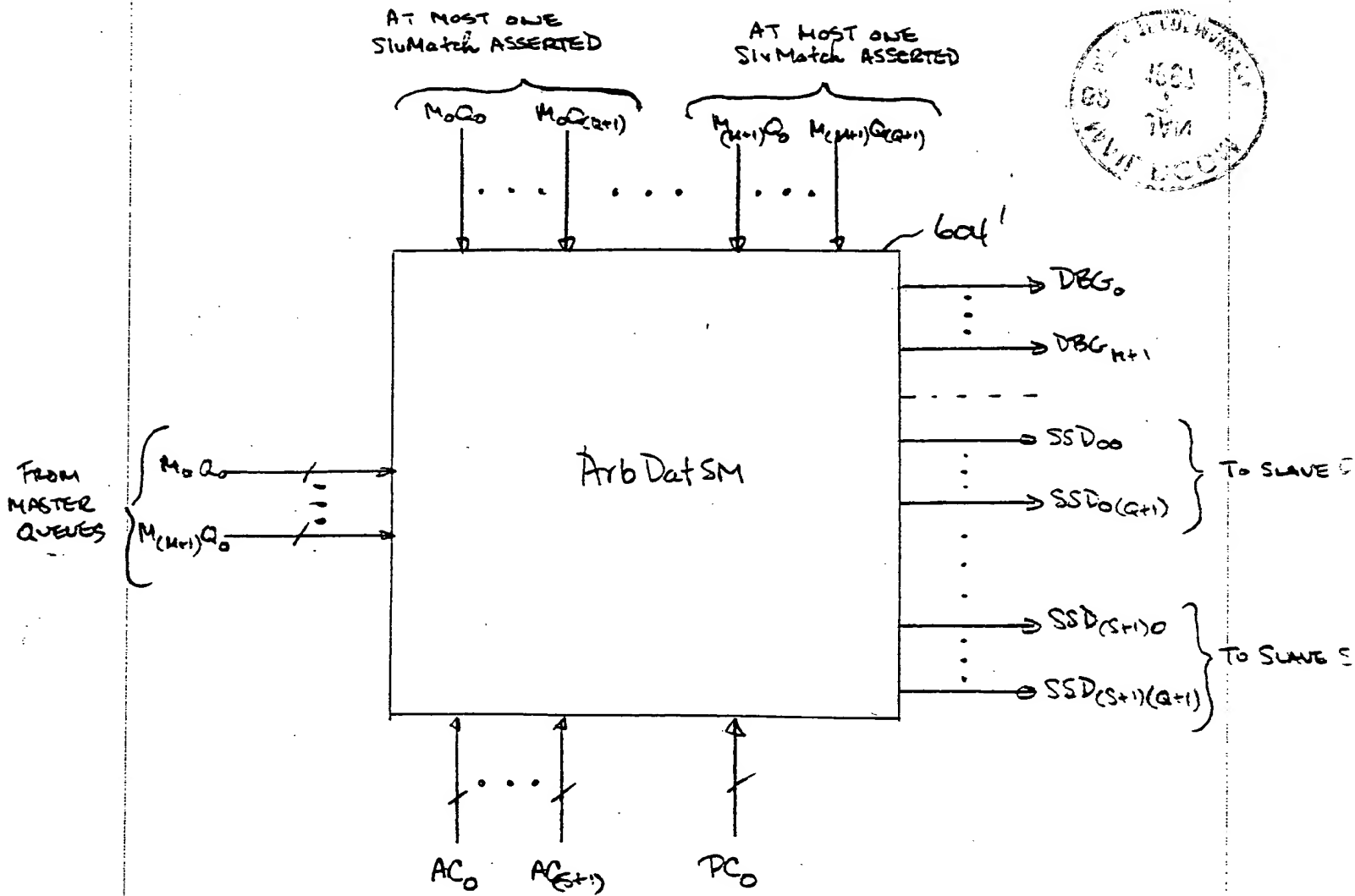


Fig 22

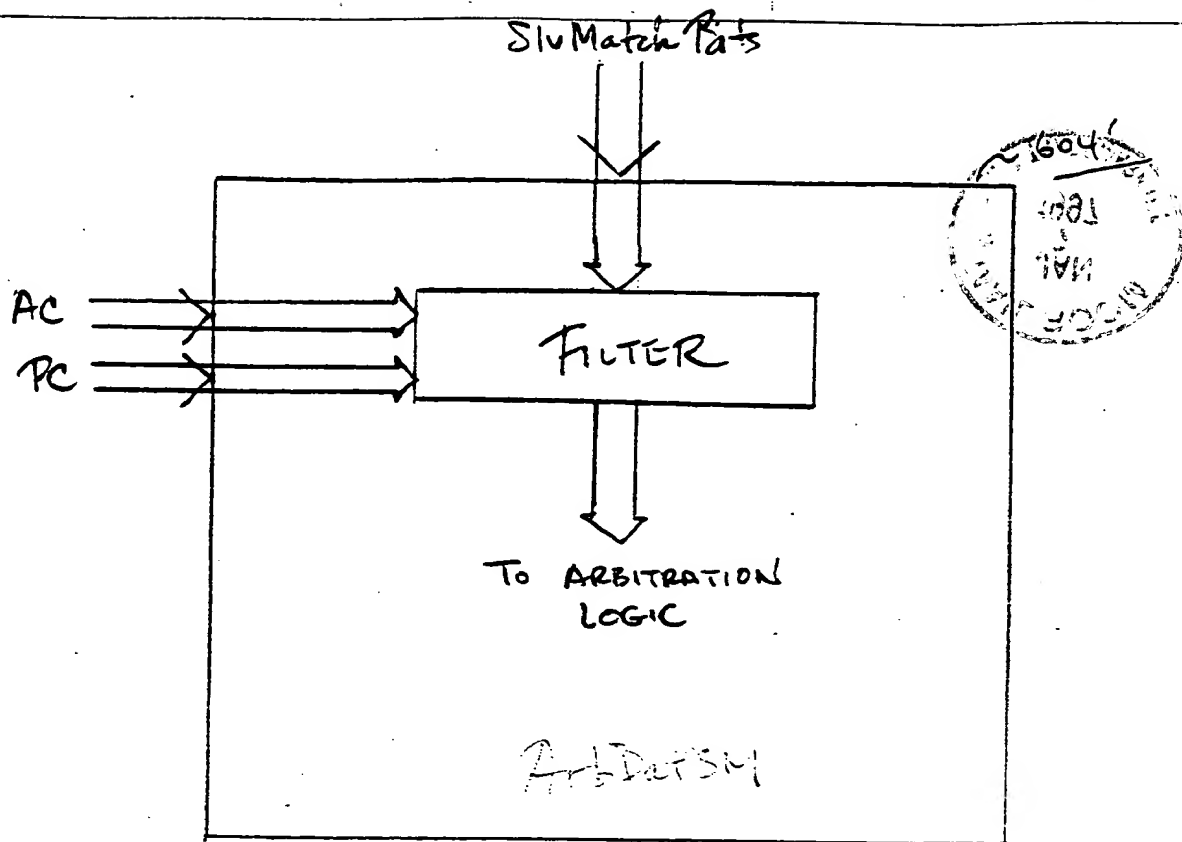


Fig 23

$M_0Q_0$  8  
 $M_1Q_0$  5  
 $M_2Q_0$  8  
 $M_3Q_0$  8  
 $M_4Q_0$  8

$M_0Q_0$   $M_0Q_1$   $M_0Q_2$   $M_1Q_0$   $M_1Q_1$   $M_1Q_2$   $M_2Q_0$   $M_2Q_1$   $M_2Q_2$   $M_3Q_0$   $M_3Q_1$   $M_3Q_2$   $M_4Q_0$   $M_4Q_1$   $M_4Q_2$   
 SLV Match  
 SLV Rd Ready  
 604

$\triangleright DRG_0$   
 $\triangleright DRG_1$   
 $\triangleright DRG_2$   
 $\triangleright DRG_3$   
 $\triangleright DRG_4$   
  
 $\triangleright SSD_{00}$   
 $\triangleright SSD_{01}$   
 $\triangleright SSD_{02}$   
  
 $\triangleright SSD_{10}$   
 $\triangleright SSD_{11}$   
 $\triangleright SSD_{12}$   
  
 $\triangleright SSD_{20}$   
 $\triangleright SSD_{21}$   
 $\triangleright SSD_{22}$   
  
 $\triangleright SSD_{30}$   
 $\triangleright SSD_{31}$   
 $\triangleright SSD_{32}$   
  
 $\triangleright SSD_{40}$   
 $\triangleright SSD_{41}$   
 $\triangleright SSD_{42}$   
  
 $\triangleright SSD_{50}$   
 $\triangleright SSD_{51}$   
 $\triangleright SSD_{52}$

Arb Dat SM



$ACD_{01}$   $ACD_{02}$   $ACD_{03}$   $ACD_{04}$   $ACD_{05}$   $ACD_{06}$   $ACD_{07}$   $ACD_{08}$   $ACD_{09}$   $ACD_{10}$   $ACD_{11}$   $ACD_{12}$   
 FROM SLAVE 0  
  
 $AC1_{01}$   $AC1_{02}$   $AC1_{03}$   $AC1_{04}$   $AC1_{05}$   $AC1_{06}$   $AC1_{07}$   $AC1_{08}$   $AC1_{09}$   $AC1_{10}$   $AC1_{11}$   $AC1_{12}$   
 FROM SLAVE 1  
  
 $AC2_{01}$   $AC2_{02}$   $AC2_{03}$   $AC2_{04}$   $AC2_{05}$   $AC2_{06}$   $AC2_{07}$   $AC2_{08}$   $AC2_{09}$   $AC2_{10}$   $AC2_{11}$   $AC2_{12}$   
 FROM SLAVE 2  
  
 $AC3_{01}$   $AC3_{02}$   $AC3_{03}$   $AC3_{04}$   $AC3_{05}$   $AC3_{06}$   $AC3_{07}$   $AC3_{08}$   $AC3_{09}$   $AC3_{10}$   $AC3_{11}$   $AC3_{12}$   
 FROM SLAVE 3  
  
 $AC4_{01}$   $AC4_{02}$   $AC4_{03}$   $AC4_{04}$   $AC4_{05}$   $AC4_{06}$   $AC4_{07}$   $AC4_{08}$   $AC4_{09}$   $AC4_{10}$   $AC4_{11}$   $AC4_{12}$   
 FROM SLAVE 4  
  
 $AC5_{01}$   $AC5_{02}$   $AC5_{03}$   $AC5_{04}$   $AC5_{05}$   $AC5_{06}$   $AC5_{07}$   $AC5_{08}$   $AC5_{09}$   $AC5_{10}$   $AC5_{11}$   $AC5_{12}$   
 FROM SLAVE 5  
  
 $PCD_{01}$   $PCD_{02}$   $PCD_{03}$   $PCD_{04}$   $PCD_{05}$   $PCD_{06}$   $PCD_{07}$   $PCD_{08}$   $PCD_{09}$   $PCD_{10}$   $PCD_{11}$   $PCD_{12}$   
 FROM SLAVE 6

Fig 24



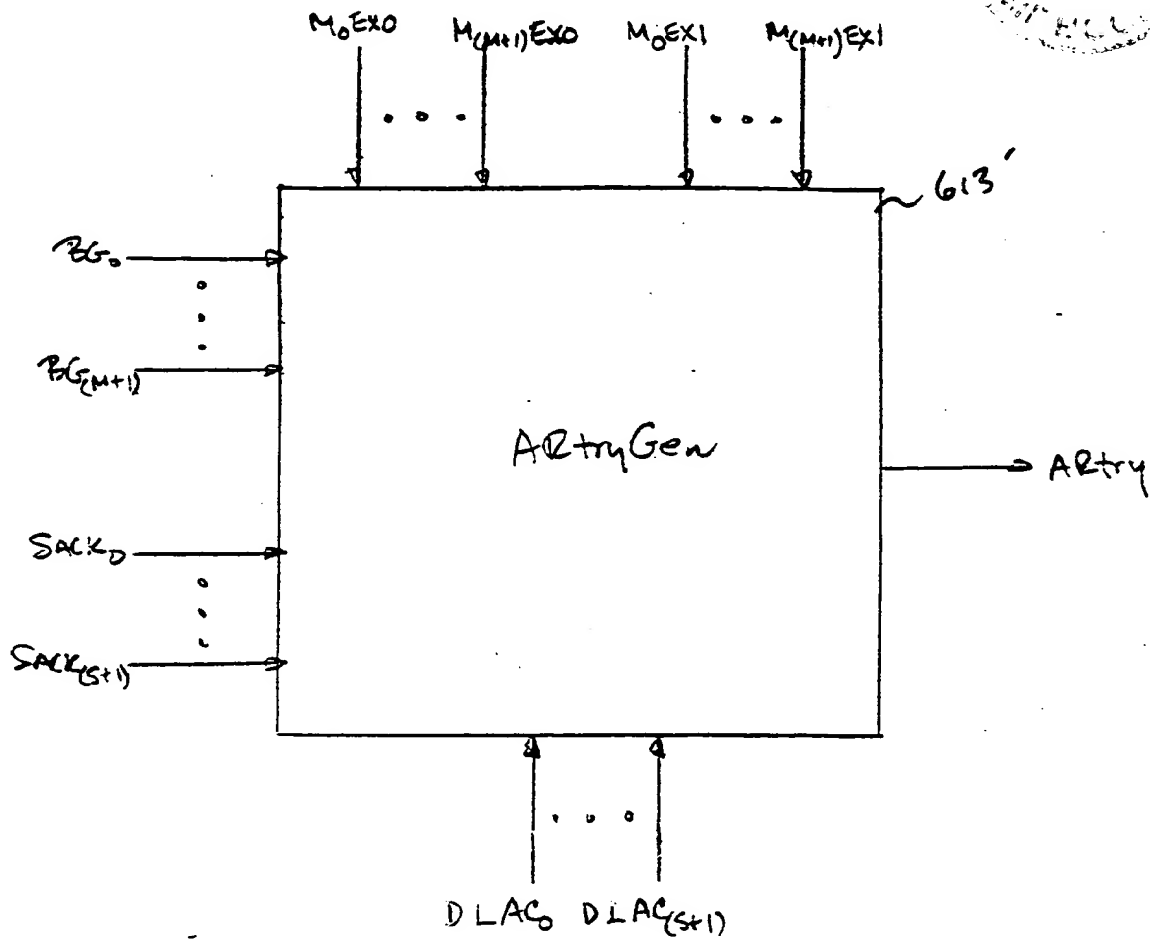


Fig 25

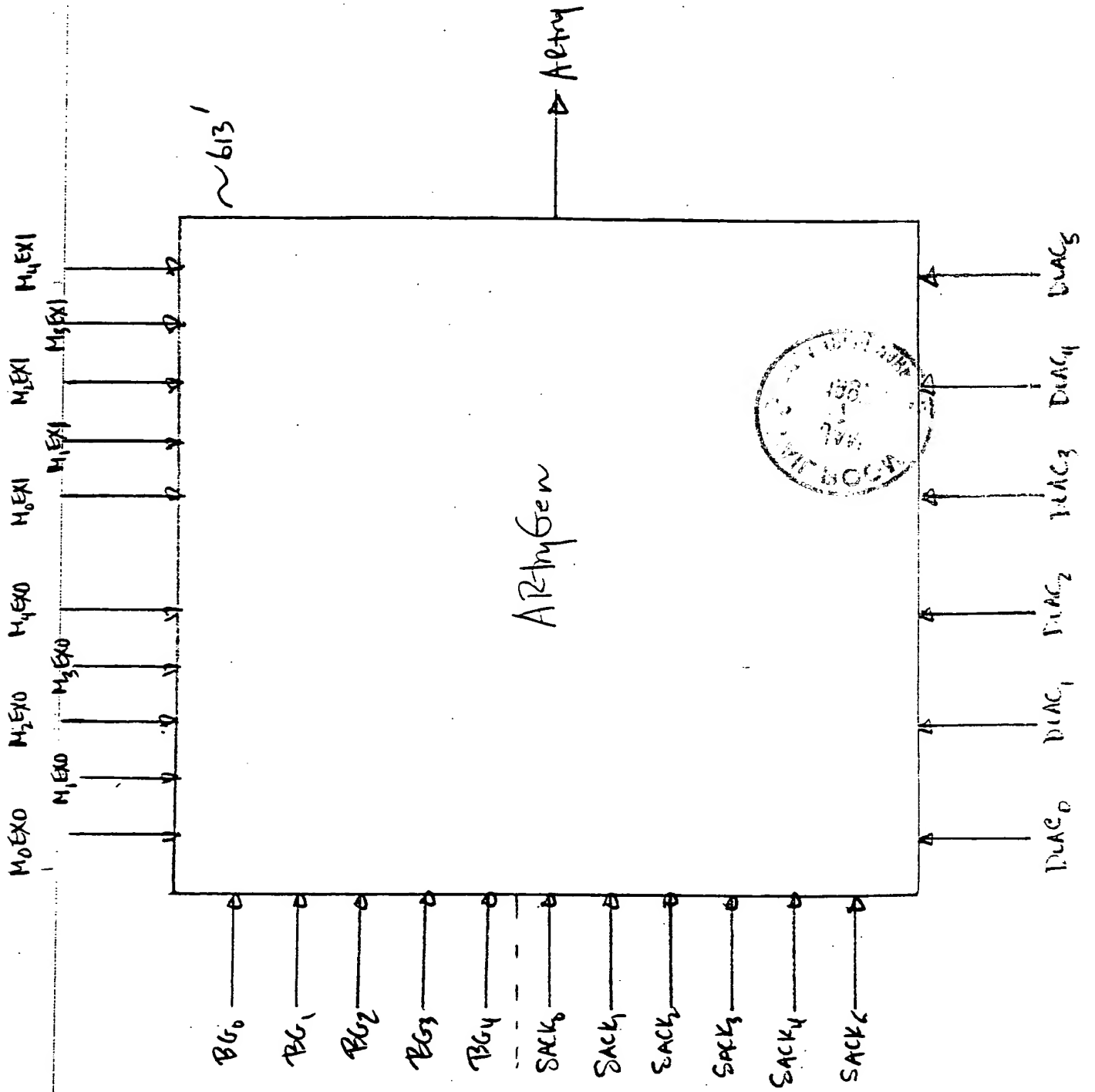


Fig 26